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## U. S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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Examiner Signature	<i>M. W. Czewski</i>	Date Considered	9/03
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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<i>Complete if Known</i>			
		Application Number			
		Filing Date			
		First Named Inventor	Kenneth R. Burch		
		Group Art Unit			
Examiner Name					
Sheet	2	of	2	Attorney Docket Number	SC11767TC

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
mw	AE	Lukas W. Snykman et al., "ME2-6", The 4 <sup>th</sup> Pacific Rim Conference on Lasers and Electro-Optics, Volume: 1, 2001, pgs. I-108 – I-109.	
mw	AF	Monuko du Plessis et al., "Silicon Light Emitting Devices In Standard CMOS Technology", Semiconductor Conference 2001 IEEE, CAS 2001 Proceedings, International, Volume: 1, 2001, pgs. 231-238.	
mw	AG	L.W. Snyman et al., "An Efficient Low Voltage, High Frequency Silicon CMOS Light Emitting Device and Electro-Optical Interface", 1999 IEEE Electron Device Letters, Vol. 20, No. 12, December, 1999, pgs. 614-617.	
mw	AH	Monuko du Plessis et al., "A Gate-controlled Silicon Light Emitting Diode", Africon 1999 IEEE, Volume 2, 1999, pgs. 1157-1160.	
mw	AI	Lukas W. Snyman et al., "Enhanced Light Emission From A Silicon N+PN CMOS Structure", Southeastcon 1999, Proceedings, IEEE, 1999, pgs. 242-245.	
mw	AJ	Lukas W. Snyman et al., "Practical Si LED's With Standard CMOS Technology", Southeastcon 1998, Proceedings, IEEE, 1998, pgs. 344-347.	
mw	AK	Lukas W. Snyman et al., "Higher-Efficiency Si LED's With Standard CMOS Technology", 1997 Workshop on High Performance Electron Devices for Microwave and Optoelectronic Applications, 1997, EDMO, pgs. 340-345.	

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